



Silicon photonics electro-optical transceiver front-ends

Abstract:

The talk will start with STMicroelectronics company presentation and will introduce the Studio di Microelettronica, the joint research lab between STMicroelectronics and the University of Pavia. A technical section will follow, presenting the on-going activities on low-cost miniaturized silicon photonics transceivers. The main applications driving the industrialization of silicon photonics will be introduced and the manufacturing of a silicon photonics platform in 300mm wafers will be analyzed. The challenges entailed by the applications will be addressed and recently realized ICs achieving state-of-the-art performance will be presented.

Speaker:

Enrico Temporiti received the Laurea degree in Electronic Engineering from the University of Pavia in 1999. In 2000 he joined STMicroelectronics in the "Studio di Microelettronica", where is currently working in the CMOS ASIC R&D Team. His interests are in the field of CMOS analog and mixed-signal high-speed integrated circuits for wireless and wireline communications.

The seminar will be **Jan. 10th 2018**, from **4 to 6 PM** in the **Seminar Room, Floor D** of the Department of Electrical, Computer and Biomedical Engineering.

