

PhD School in Microelectronics

Industrial Topics in Microelectronics and Photonics - Seminars

Design Verification Methodologies

December 1st, h 16.00, Magenta Seminar Room (D Floor)

Zoom Link for remote connection

Department of Electrical, Computer and Biomedical Engineering

Abstract: Microelectronics devices are increasing their complexity since their origin and device errors and faults ("bugs") are a natural consequence of complex system design; the verification phase is aimed at finding all possible bugs, saving the cost and time of new design spins. During this seminar we will go in deep details through the reasons for which Design Verification has been introduced and we will walk through the methodologies currently used in semiconductor industries.

Speaker:

Alessio Pelle

He received his M.S. degree in Electrical Engineering from Genova University in 1996. After some experiences in various companies, he joined Accent S.R.L. as a Digital Designer in 2000 dealing with different projects, ranging from Application Processor SoC to LED driver devices. From 2008 to 2016 he worked at Marvell Semiconductor, Pavia, in the area of Power Management IC for mobile devices. In 2016 he joined TDK-InvenSense, where he currently covers the role of Senior Manager for Verification, contributing to build the verification methodology for Microphone devices.

• Fabio Brognara

He received his M.S. degree in Computer Science from Pavia University in 2003. He started his experience in verification in 2005 in ST Microelectronics after 2 years as digital designer. He has experience both in IP and SoC verification products targeting consumer market, networking and Mixed-Signal. In 2016 he joined TDK-Invensense, where he currently covers the role of Senior Manager for Verification, contributing to build the verification methodology for Motion Sensor devices.

Organizer Prof. D. Manstretta E-mail: <u>danilo.manstretta@unipv.it</u> PhD Coordinator Prof. P. Malcovati