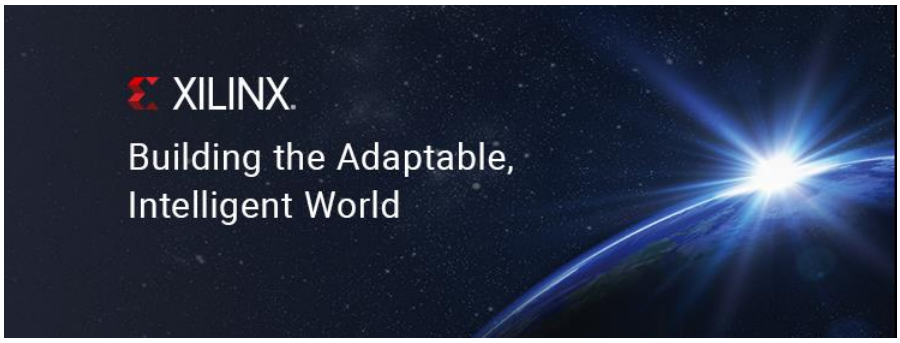
UNIVERSITÀ DI PAVIA
Department of Electrical, Computer and Biomedical Engineering

PHD SCHOOL IN MICROELECTRONICS

***Industrial Topics in Microelectronics - Seminars***




# ADC based Receiver for 112Gb/s PAM4 Serial Link

**October 6th 2021, h 16.00**

**Zoom Link for remote connection**

Department of Electrical, Computer and Biomedical Engineering

***Abstract:*** *The presentation covers the need of high-speed electrical links in data center, showing the growth of Global data center IP traffic. It shows then the limitations of NRZ modulation for high speed serial communication links and alternative to mitigate the limitations. Finally, it covers the real implementation in 7nm FinFet technology of a receiver that can run up to 134Gb/s in a serial link communication system.*

**Speaker: Frantz Ngankem** was born in 1990 in Camerun. He received his BE in electronic and telecommunication engineering from the University of Pavia in 2013 and the Master's degree in electronic Engineering (Microelectronics) from the University of Pavia as well in 2015. As part of his Master's degree he was an Intern as an Analogue Mixed Signal Design Engineer for battery management system in Freescale Semiconductors of Toulouse in France (now NXP). On January 2016 he Joined Xilinx, Ireland Cork as an Analogue Design Engineer with the SerDes technology group where he is involved in the design of circuits for High-speed serial links.

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