



UNIVERSITÀ DI PAVIA
Department of Electrical,
Computer and Biomedical
Engineering

PHD SCHOOL IN MICROELECTRONICS



Extremely High Frequency Integrated Circuits for Emerging Communication Networks

June 8th 2020, h 16.00

Web meeting url: <https://welink-meeting.zoom.us/j/568643514>

Department of Electrical, Computer and Biomedical Engineering

Abstract:

The focus of this presentation will be on very advanced radio frequency integrated circuits that have been developed in Huawei Milan R&D Centre.

The increasing demand for higher bandwidth in telecommunication networks is pushing the industry to develop wireless and wireline communication systems with much higher capacity than the existing ones.

Millimeter wave carriers and MIMO approach are expected to be extensively employed in 5G and Point-to-Point wireless systems, while 400-Gb/s data rate per channel is emerging as a promising step in the evolution of Optical transport systems to sustain the traffic growth, improve spectral efficiency, and lower cost per bit in fiber transmission. Some practical example of components designed in our lab with their challenges and possible evolutions will be introduced.

Speaker: Federico Vecchi

Federico Vecchi was born in Broni, Italy, in 1982. He received the M.S. and Ph.D. degrees in electronics engineering from the University of Pavia, Italy, in 2006 and 2010, respectively. In 2006 he joined the Department of Electronics, University of Pavia, as a Ph.D. student in microelectronics. In 2007 he spent a period at the Berkeley Wireless Research Center, University of California, Berkeley, for the characterization of integrated CMOS devices for millimeter-wave applications. In 2010 he got a post-doctoral position from the University of Pavia working on frequency synthesis for serial interfaces applications. He joined Huawei Technologies, Italy in 2011; since then, he's been involved in the design of frequency synthesizers, building blocks for 5G applications (LNA, Mixer, PA), TIAs and Drivers for Optical RFIC applications in CMOS and BiCMOS technologies.

Organizer

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