

PhD School in Microelectronics



Test Development Engineering in Infineon

April 27th 2020, h 16.00

Skype meeting url: https://meet.lync.com/unipvit-unipv/danilo.manstretta/863RPHZ1
Department of Electrical, Computer and Biomedical Engineering

Abstract:

An overview of Test Development activity in Infineon is given, with particular focus on Pavia design center, in which the test development team is present since the foundation in 2001. Test concept is introduced and its fundamental role for the electronic integrated circuits is shown. Automatic Test Equipment main features are explained, test solution phases are analyzed with focus on the IC Gate Driver products development flow.

Speaker: Daniela Barge

- Graduated in MSEE/Telecommunication in University of Pavia on March 24th, 2000
- ➤ May 2000-March 2012 : Characterization Engineer in ST Microelectronics (BCD and optolelectronic devices, MEMS)
- > March 2012- now: Test Engineer in Infineon (former IR) Pavia Laboratories

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