



UNIVERSITÀ DI PAVIA
Department of Electrical,
Computer and Biomedical
Engineering

PHD SCHOOL IN MICROELECTRONICS



NAND Flash Memories: an increasing challenge

Nov 20th 2019, h 16.00, Magenta Seminar Room (D Floor)

Department of Electrical, Computer and Biomedical Engineering

Abstract: Memories are one of the major driving forces of the semiconductor industry; they accounted for over 100 B\$ in sales over a total market of 416 B\$ in 2017 alone.

Semiconductor memory systems based on NAND Flash and DRAMs are everywhere around us (Personal Computers, Mobile Devices, Data Centers, ...) and will be among the key players in the emerging applications of the near future such as Internet of Things (IoT), Artificial Intelligence (AI) and mobility (autonomous driving).

Memories are subject to a relentless market push for increased density and performance, as well as reduced cost, thus they are at the frontline of technology and design innovation.

SK hynix, a world leader on semiconductor memory systems, will highlight some of the increasing challenges of multiple-bit-per-cell NAND flash memories.

The Company and the speakers : SK hynix Italy (SKHYIT) is SK hynix European R&D centre on NAND flash . Located near Milan, SKHYIT mission is to develop high density, high performance multiple-bits-per-cell flash memories in the state-of-art 3D NAND technology.



Jacopo Mulatti : Born in 1967, MS degree in Electronic Engineering.
Since 1995 he has worked on Flash Memory Design in STMicroelectronics, Numonyx and Micron Technologies.
In 2014 he joined SK hynix as SK hynix Italy General Manager.



Stefano Sivero: Born in 1974, MS degree in Electronic Engineering and Physics.
Since 2003 he has worked on Flash Memory Design in STMicroelectronics, Atmel, Spansion and Elpida.
He joined SK hynix Italy in 2012 ; his current position is Senior Manager of Core team and Project Leader.

Organizer

Prof. D. Manstretta

E-mail: daniilo.manstretta@unipv.it

PhD Coordinator

Prof. P. Malcovati