NAND FLASH MEMORIES : a continuous challenge

Memories are one of the major driving forces of the Semiconductor Market, accounting for over 100 B$ in sales in 2017 alone.

Semiconductor memories are everywhere around us (PCs, Mobile Devices, Big Data Clouds…) and will be key players in the coming age of IC Tech trends, such as IoT, A.I. and mobility.

SK hynix, one of the world's leader in memories and memory systems, will highlight the increasing technology and design challenges of multiple-bit-per-cell NAND flash memories.

Jacopo Mulatti
25 years experience in Flash Memory design (NOR and NAND flash) .
2003 - 2010 : Design Centre Manager in Seoul (South Korea) within the STMicroeletronics-Hynix Joint Venture in NAND flash memories.
2010 - 2014 : NAND architect in Micron Technologies, Wireless Solution Group.
2014 - : General Manager of SK hynix Italy, the European R&D centre on multiple-bit-per-cell NAND flash

Stefano Sivero
19 years experience in Flash Memory design (NOR and NAND flash) .
2003 - 2008 : Senior Analog Design in STMicroelectronics and Atmel, NOR Flash Design
2008 – 2012 : Analog and Layout leader in Spansion / Elpida , NAND Flash Design
2012 - : Core Team Senior Manager and Project Leader on 3D-NAND flash, SK hynix Italy

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