

DISTINGUISHED TALKS INSIGNIA COLLOQUIA

**Eng. Frantz Stephane
Ngankem**

**IC Analog Mixed Signal Design Engineer
Xilinx SerDes
Cork (Co), Irland**



Seminar

**Xilinx FPGA
in High Speed Serial Links**

WHEN

May 21st, 2018 – 11 am

WHERE

**Aula Seminari – D floor
Ex Dipartimento di Elettronica**

**Via Adolfo Ferrata, n. 5 – Edificio “Nave”
27100 Pavia**

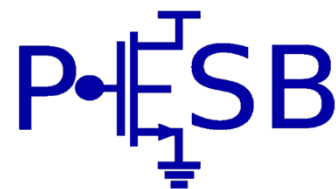
**Seminar co-organized with the PhD School of
Microelectronics
Coordinator: Prof. Guido Torelli**

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