





AMS: Company overview, ASIC development flow and testing activities in Pavia Design Center

Abstract:

The seminar will first introduce AMS with emphasis on the company design center located in Pavia. The talk will then present a full project development flow for new products, starting from feasibility studies until product industrialization, considering all the required activities, in addition to pure circuits design and layout. In the second part of the seminar, a deeper insight is provided concerning the testing activities along each phase of the project.

Speakers:

Name	Photo	Education	Main work experiences
Marco Cerchi		Electronic Engineering Laurea Degree @ Pavia University	2000-2001 Analog Designer @ ST-Microelectronics Main focuses: development of high-frequency sigma-delta D/A converters and PLLs 2001-2004 Analog Designer @ MIKRON Italy Main focuses: development of battery charge solutions and sensor interface ASICs (inductive sensors, smoke detectors, touch-screens, bar-code readers) 2004- 2016 Senior Designer @ AUSTRIAMICROSYSTEMS Italy (AMS Italy since 2012) Main focuses: development of SL LDOs, μ Processor supervisors and Hall effect magnetic position sensors 2016- so far Engineering Manager Pavia @ AMS Italy
Thomas Beghelli		Electronic Engineering Laurea Degree @ Perugia University	2000-2004 Product Engineer @ ST-Microelectronics Main focuses: electrical wafer sort on FLASH memories 2004-2006 Senior Test Engineer @ ST-Microelectronics Main focuses: development of MCP (MultiChipPackage) engineering/testing/RMA analysis 2006-2008 Senior Test Engineer @ Austriamicrosystems AG/Italy 2008-so far Test Development Manager @ Austriamicrosystems AG/Italy (AMS Italy since 2012) Main focuses: Test HW/SW development for LTX/Fusion ATE systems (EX,DX,MX) of Power Management, Wireless and Automotive devices

The seminar will be **May 8th**, from **4 to 6 PM** in Aula seminari, floor D.



This seminar is co-sponsored by the IEEE Circuits and Systems Society and it is part of the IEEE CASS North Italy Chapter activity.