



An IEEE CASS Outreach Initiative STAR-CAS: Selected Topics on Advanced Research on Circuits and Systems

University of Pavia - September 28th-30th 2015
Aula Foscolo - Corso Strada Nuova, 65 - Pavia

Monday September 28th

9.00 – 12.00

Silicon photonics: technology, optical devices and applications

G. Chiaretti, A. Fincato, G. Temporiti – STMicroelectronics

14.00 – 17.00

Design and testing strategies for modular 3D-multiprocessor systems using die-level TSV technology

Prof. Y. Leblebici – École Polytechnique Fédérale de Lausanne

Tuesday September 29th

9.00 – 10.30

On the design of circuits for frequency synthesizers at mm-waves in ultra scaled CMOS

Prof. F. Svelto – University of Pavia

10.30 – 12.00

Power efficient SAR A/D converters

Prof. F. Maloberti – University of Pavia

14.00 – 17.00

Architectures and circuits for neural recording interfaces

Prof. A. Rodriguez Vazquez – University of Sevilla

Wednesday September 30th

9.00 – 12.00

Sigma-Delta converters: fundamentals, state of the art, and applications

Prof. J.M. de la Rosa – IMSE-CNM (CSIC / University of Sevilla)

14.00 – 17.00

State of the art in the CMOS image sensor development

R. Bez – LFoundry

Check out the STAR-CAS website for latest news

<http://ims.unipv.it/STARCAS>

Participation is free, but, because the historical Aula Foscolo has limited occupancy, the maximum number of participants is 70. To register and information, send an e-mail to: star-cas2015@gmail.com